

Curriculum Vitae

Ph. D. Byungjin Cho



Department of Advanced Material engineering

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Education & Experience

- ✧ **Assistant Professor**, Department of Advanced Materials Engineering (2017.03 ~ present)
Chungbuk National University, Korea
- ✧ **Senior Researcher**, Department of Advanced Functional Thin Films Department (2013.02 ~ 2017.02)
Korea Institute of Materials Science (KIMS), Korea
- ✧ **Postdoctoral Researcher**, Department of Mechanical and Aerospace Engineering (2012.03~2012.12)
University of California, Los Angeles (UCLA), USA (PI: Prof. Yong Chen)
- ✧ **Ph. D.** Department of Materials Science and Engineering (2009.03~2012.02)
Gwangju Institute of Science and Technology (GIST), Korea
Thesis: Organic Resistive Memory Devices: Switching Mechanisms, Performance Enhancement, and Integration Architectures
Supervisors: Prof. Takhee Lee (2009.03~2011.09) and Prof. Gun-Young Jung (2011.09~2012.02)
- ✧ **MS**, Department of Materials Science and Engineering (2007.03~2009.02)
Gwangju Institute of Science and Technology (GIST), Korea
Thesis: Unipolar Nonvolatile Memory Devices with Composites of Poly(9-vinylcarbazole) and Titanium Dioxide Nanoparticles
Supervisor: Prof. Takhee Lee
- ✧ **Process Engineer** (2005.07~2007.02), Manufacturing Technology 9 TEAM
Hynix Semiconductor, Korea
- ✧ **BS** (1998.03~2005.02), Department of Materials Science and Engineering
Chungbuk National University, Korea

Awards and Honors

- ✧ KIMS 인재상, 재료연구소: 2016.04.25

- ✧ Best oral presentation Award, MRS Korea: 2013.05
- ✧ Brain Korea 21 Outstanding Research Award Winner: 2012.12
- ✧ Dasan Project Fellowship, GIST, Korea: 2011
- ✧ Best Poster Presentation Award, GJ-NST 2009: 2009.11
- ✧ Scholarship supported by Korean government: 2007 ~ 2012
- ✧ Brain Korea 21 Fellowship: 2007~2012

Research Interest

- ✧ CVD synthesis of 2D atomic-layered materials and their electronic device applications
- ✧ CNT synaptic devices for neuromorphic circuit applications
- ✧ Organic based resistive memory devices: mechanism, performance enhancement, integration architecture

Summary of Achievements

- ✧ **58** Research Publications, **1** Book Chapter
- ✧ **21** Patents
- ✧ **62** Presentations

